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## U.S. UTILITY Patent Application

<b>M.S.</b>	<b>O.I.P.E.</b>	<b>PATENT DATE</b>
SCANNED <u>TR4</u> Q.A. <u>411</u>		

<b>APPLICATION NO.</b> 09/893598	<b>CONT/PRIOR</b> F	<b>CLASS</b> <del>257</del> 345	<b>SUBCLASS</b> 211	<b>ART UNIT</b> <del>2811</del> 2675	<b>EXAMINER</b> BELL
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**APPLICANTS** Yoshinori Uchiyama

**TITLE** Semiconductor circuit in which power consumption is reduced and semiconductor circuit system using the same

PTO-2040  
12/88

### ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
<b>INTERNATIONAL CLASSIFICATION</b>							

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	_____ (Assistant Examiner) _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>  _____	
	_____ (Primary Examiner) _____ (Date)			<b>ISSUE FEE</b> Amount Due _____ Date Paid _____	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)			<b>ISSUE BATCH NUMBER</b>  _____	

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